

# Handshake Circuits

An asynchronous  
architecture for VLSI  
programming

Kees van Berkel

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Cambridge International Series  
on Parallel Computation

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# Handshake Circuits An Asynchronous Architecture For Vlsi Programming

**International Conference on VLSI  
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## **Handshake Circuits An Asynchronous Architecture For Vlsi Programming:**

Handshake Circuits Kees van Berkel,2004-12-16 Describes a new way of designing VLSI circuits by programming illustrated with examples *Asynchronous System-on-Chip Interconnect* John Bainbridge,2013-11-11 Asynchronous System on Chip Interconnect describes the use of an entirely asynchronous system bus for the modular construction of integrated circuits Industry is just awakening to the benefits of asynchronous design in avoiding the problems of clock skew and multiple clock domains an din parallel with this is coming to grips with Intellectual Property IP based design flows which emphasise the need for a flexible interconnect strategy In this book John Bainbridge investigates the design of an asynchronous on chip interconnect looking at all the stages of the design from the choice of wiring layout through asynchronous signalling protocols to the higher level problems involved in supporting split transactions The MARBLE bus the first asynchronous SoC bus used in a commercial demonstrator chip containing a mixture of asynchronous and synchronous macrocells is used as a concrete example throughout the book

**Mathematics of Program Construction** Johan Jeuring,1998-05-27 This book consists of the refereed proceedings of the 4th International Conference on Mathematics of Program Construction MPC 98 held in Marstrand near Goteborg Sweden in June 1998 The 17 revised full papers presented were selected from 57 submissions also included are three invited contributions The volume is devoted to the use of crisp clear mathematics in the discovery and design of algorithms and in the development of corresponding software and hardware various approaches to formal methods for systems design and analysis are covered

**Embedded Systems Specification and Design Languages** Eugenio Villar,2008-05-15 This book is the latest contribution to the Chip Design Languages series and it consists of selected papers presented at the Forum on Specifications and Design Languages FDL 07 in September 2007 The book represents the state of the art in research and practice and it identifies new research directions It highlights the role of specification and modelling languages and presents practical experiences with specification and modelling languages

**Communicating Sequential Processes. The First 25 Years** Ali E. Abdallah,2005-05-03 This book commemorates the work done by Tony Hoare and published under the title *Communicating Sequential Processes* in the 1978 August issue of the *Communications of ACM* The British Computer Society's specialist group Formal Aspects of Computing Science organized a meeting on July 7 8 2004 in London to mark the occasion of 25 years of CSP The 19 carefully reviewed and revised full papers by leading researchers celebrate reflect upon and look beyond the first quarter century of CSP's contributions to computer science The papers explore the impact of CSP on many areas ranging from semantics and logic through the design of parallel programming languages to applications varying from distributed software and parallel computing to information security Web services and concurrent hardware circuits

Programming Methodology Annabelle McIver,Carroll Morgan,2002-10-01 Written by the members of the IFIP Working Group 2.3 Programming Methodology this text constitutes an exciting reference on the front line of research activity in programming methodology The range of

subjects reflects the current interests of the members and will offer insightful and controversial opinions on modern programming methods and practice The material is arranged in thematic sections each one introduced by a problem which epitomizes the spirit of that topic The exemplary problem will encourage vigorous discussion and will form the basis for an introduction tutorial for its section

**Asynchronous Design Methodologies** Stephen Bo Furber, Martyn D. Edwards, 1993 This publication reflects the current state of the art in asynchronous design which is currently enjoying a world wide resurgence of interest The papers contributed by a diverse selection of international specialists in the field offer a balance of theory and engineering practice They are organised into three sections with the themes Synthesis and Modelling Theory and Specification and Engineering Practice It is hoped that with its broad ranging approaches to the subject the book will serve as an accurate record of the state of development in 1993 of what may in the future be a commercially significant technology Hopefully it will also in the meantime encourage further research by the computer VLSI designers and postgraduate students of today

**Half a Century of Inspirational Research** Wojciech Penczek, Grzegorz Rozenberg, 2007 Honors Professor Antoni Mazurkiewicz who during his long scientific career made fundamental contributions to theoretical computer science This book includes contributions which span a range of research areas including the theory of programming models of concurrent and distributed systems and decomposition methods for Petri nets

The Design and Implementation of an Asynchronous Network on a Chip IV Kelly, 2005

**2nd International Symposium on Advanced Research in Asynchronous Circuits and Systems** Institute of Electrical and Electronics Engineers, Inc. Staff, 1996-03 Papers from the March 1996 symposium detail the latest knowledge in asynchronous hardware design in sections on high speed design logic synthesis architectural synthesis formal methods novel techniques design automation and measurements low power and system design and logic optimization

The **Self-timed Circuit Design** Juha Plosila, 1999 *Proceedings ... International Symposium on Asynchronous Circuit and Systems*, 2002

**18th International Conference on VLSI Design** International Conference on VLSI Design, 2005

**VLSI: Integrated Systems on Silicon** Ricardo A. Reis, Luc Claesen, 1997-08-31 VLSI very large scale integration technology is evolving to the point where complete systems are being integrated on a single chip including ASICs embedded processing hardware and software control analog and digital signal processing sensors and actuators In addition there is a demand for many systems to be compact portable with wireless communication efficient production and low battery power operation This book presents novel developments addressing the conception design CAD and realization of such new systems Among the important issues presented are VLSI systems on a chip electronic design automation ultra low voltage and low power automatic synthesis VLSI applications The book comprises the selected proceedings of the International Conference on Very Large Scale Integration VLSI 97 which was sponsored by the International Federation for Information Processing IFIP and was held in Gramado Brazil in August 1997 It is a state of the book for researchers and managers working on system integration design and CAD

VLSI, 1993 **Second**

**International Symposium on Advanced Research in Asynchronous Circuits and Systems** ,1996    Seventh  
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*Prototyping of Asynchronour Logic* John Russel Teifel,2004    **Mathematics of Program Construction** ,1998

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